

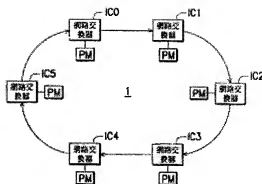
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Patent/Publication
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using dual-port memoryIssued/Publication
Date 2002/08/11

Application Date 1998/10/30

Application
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Number 162031IPC G06F-015/163(2006.01);
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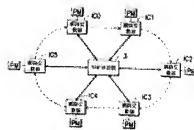
Abstract Ring topology network switch based on the invention utilizes dual-port memory for common packet memory of two network switches. When packet transmission is intended between the two network switches, the ring bus path transmission only needs control data. Furthermore, independent control bus can also be installed in between the two network switches, making it require no ring bus path in between the network switches when packet transmission is intended



第 1 圖



第 2 圖



第 3 圖

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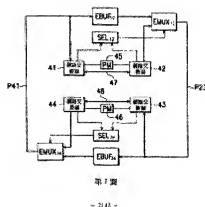
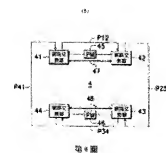
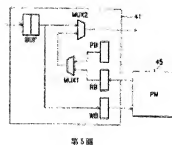
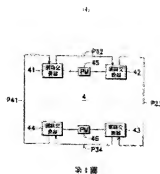
between network switches.
The two network switches can be further simplified as a single network node. Thus, the network switch of the invention can enhance the operation efficiency of the entire ring topology bus.

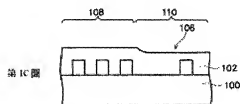
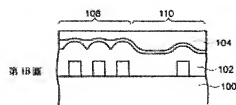
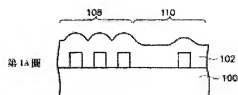
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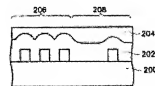
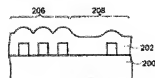
Patent Right
Change

Application Number	087118021
Date of Update	20090827
Granting of a license	No
Registration of patent mortgage	No
Transfer of patent right	No
Succession of patent right	No
Registration of patent trust	No
Opposition filed	No
Request for Invalidation filed	No
Date of lapse	
Patent revoked	
Date of grant	20020811
Scheduled expiry date	20181029
Due date of annual fee	20100810
Years of annuities paid	008





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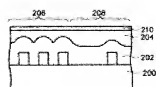


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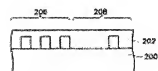


圖 15

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